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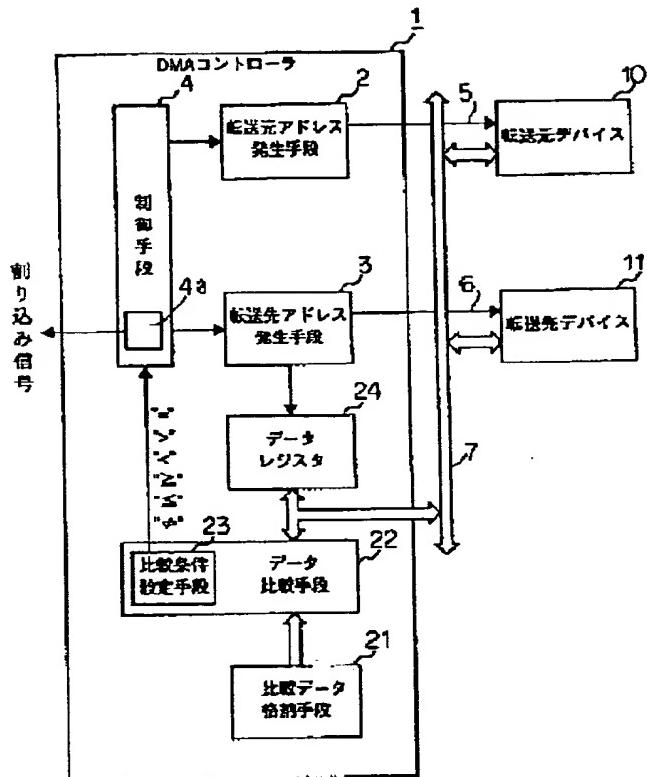
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APPLICANT : MITSUBISHI DENKI SEMICONDUCTOR
SOFTWARE KK;

INVENTOR : OHASHI SHINICHIRO;

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TITLE : DMA CONTROLLER



ABSTRACT : PURPOSE: To obtain a DMA controller capable of setting up interruption generating timing in a period optimum to data analysis, data processing or the like to be executed by a CPU and accelerating data retrieval without loading the CPU.

CONSTITUTION: The DMA controller is provided with a data register 24 for storing data selected by a transfer destination address generating means 2 and transferred from a data transfer source, a data comparing means 22 for comparing data stored in the register 24, a comparing condition setting means 23 for setting up a comparing condition, and a signal generating/outputting means 4 for generating an interruption signal in accordance with the compared result of the means 22 and outputting the signal.

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